

In collaboration with Xilinx, Inc. and ASICS World Services MLE offers the Zynq SATA Storage Extension (Zynq SSE).

Delivered as a complete reference design for the Xilinx Zynq-7000 All Programmable SoC this includes a pre-validated SATA storage subsystem with integrated Xilinx PetaLinux drivers and a fully functional ASICS World Services SATA Host Controller IP core instance for one single SATA port.

Key Features

- One single SATA I / II / III host port for 1.5/3.0/6.0 Gbps speeds.
- Access one single SATA HDD or SATA SSD via a standard Linux block device interface and Linux filesystems.
- Fully integrated and tested SATA I/II/III DMA Host Controller IP Core.
- Storage micro architecture for Zynq.
- SATA driver for Linux/Petalinux available in binary and open source.
- Less than 3% resource utilization on a Zynq-7045 leaves more than 95% of Zynq resources for you!

Zynq-7000 All Programmable SoC

Xilinx Zynq®-7000 All Programmable SoCs comprises a dual-core ARM Cortex A9MP ASSP with FPGA logic to enable flexibility through hardware, software, and I/O programmability.

License Model and Cost

The license fee structure reflects the needs of Zynq's Embedded Linux users for simple and affordable SATA. Pricing for a fully paid-up for, royalty-free, world-wide, single-project-use license, synthesizable for 1 year

- **Expert Edition 9,800.00 USD**
- **Professional Edition 14,800.00 USD**

For easy integration Zynq SSE is delivered as a complete Xilinx Vivado 2016.4 design project of the storage subsystem, plus PetaLinux 2016.4 (kernel 4.6) software.

Multiple SATA host ports, PL-connected SATA host ports, Multi-Use Project Licenses and Source Code licenses available upon request!

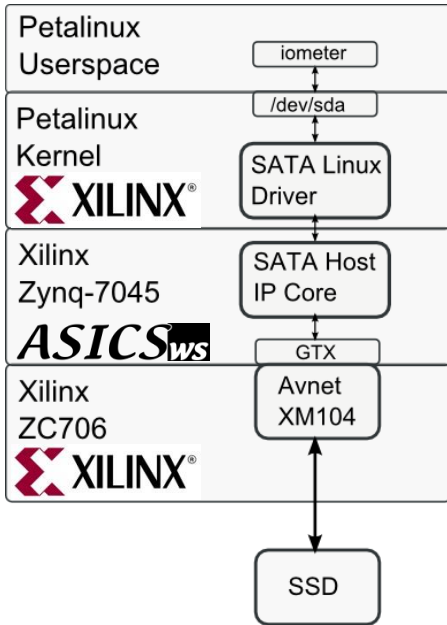
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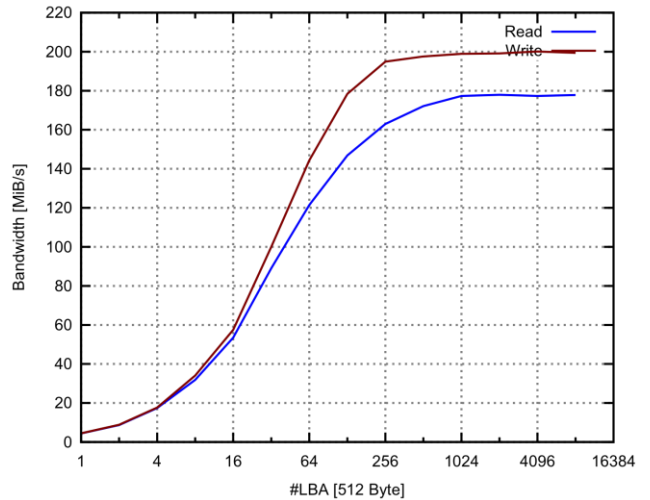
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Complete SATA System Stack



I/O Performance Benchmark



Performance data measured on an Z7045-2 with ARM A9MP core running at 700 MHz.

How to get started? Go online to <http://MLEcorp.com/ZynqSSE>

- Request a free-of-charge fully functional evaluation for the Avnet Zynq mini-ITX boards 7Z045 or 7Z100.
- Visit the Xilinx Wiki pages for Zynq SSE on how to set up an evaluation for Zynq SSE.
- Visit the Xilinx Wiki pages for Zynq SSE and read the complete design user guide explaining how to integrate Zynq SSE into your target system.

